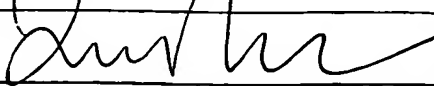


Substitute Form PTO-1449 (Modified) Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 14699-019001	Application No. 10/823,004
	Applicant Takao Myono et al.		
	Filing Date April 13, 2004	Group Art Unit 2816	

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
<u>U</u>	AA	6,515,535	02/04/2003	Myono	327	536	
	AB						
	AC						
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	AH						
	AI						
	AJ						
	AK						

Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation Abstract	
							Yes	No
<u>U</u>	AL	2001-286125	10/12/2001	Japan			X	
	AM							
	AN							
	AO							
	AP							

Other Documents (include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
<u>U</u>	AQ	J. F. Dickson, "On-Chip High-Voltage Generation in MNOS Integrated Circuits Using an Improved Voltage Multiplier Technique", IEEE Journal of Solid-State Circuits pp 374-378 (1976)
	AR	
	AS	
	AT	

Examiner Signature 	Date Considered 8/30/05
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	